




PATENT
Docket No. 20063/OG03-043

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicants: Seung Hee JWA)	I hereby certify that this paper is
)	being deposited with the United
Serial No.: 10/749,489)	States Postal Service with
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Filed: December 30, 2003)	mail in an envelope addressed to:
)	Commissioner for Patents, P.O.
For: Methods of Manufacturing AND-Type)	Box 1450, Alexandria, VA 22313-
Flash Memory Devices)	1450 on this date:
)	
Group Art Unit: Unknown)	March 2, 2004
)	
Examiner: Unknown)	Mark C. Zimmerman
)	Registration No. 44,006
)	Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed with the application or within three months of the filing date of the application or date of entry into the national stage of an international application or, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits, whichever event occurs last. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

☒ No fee is required.


The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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Respectfully submitted,

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March 2, 2004

Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

20063/OG03-043

Serial No.

10/749,489

Applicant

Seung Hee JWA

Filing Date

December 30,
2003

Group Art Unit

Unknown

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)



U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,566,195	May 20, 2003	Method and Structure for an Improved Floating Gate Memory Cell	438	257	February 6, 2002
	A02	6,326,263	December 4, 2001	Method of Fabricating a Flash Memory Cell	438	257	August 11, 2000
	A03	6,153,494	November 28, 2000	Method to Increase the Coupling Ration of Word Line to Floating Gate by Lateral Coupling in Stacked Gate of Flash	438	424	May 12, 1999

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.